

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|---|------------------|---------|---------------------|
| S1 | 1 | 10/805182 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/27 13:42 |
| S2 | 1026 | 714/5.ccds. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/02 10:34 |
| S3 | 38 | S2 AND syndrome | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/27 13:42 |
| S4 | 2 | "5922080".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/27 13:57 |
| S5 | 1 | 2001-175542 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/27 13:43 |
| S6 | 0 | S2 AND independant\$2.ab. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/27 13:57 |
| S7 | 37 | S2 AND independent\$2.ab. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/27 14:53 |
| S8 | 110 | S2 AND ((separate\$2 OR independent\$2) WITH ("same")) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/27 14:54 |

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|-----|--------|--|---|----|----|---------------------|
| S9 | 1 | "03425171.0" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/29 16:43 |
| S10 | 1 | 2002-79269 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/01/02 09:10 |
| S11 | 240 | 714/764.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/01/05 09:06 |
| S12 | 42 | S11 AND hamming ADJ code | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/01/05 09:06 |
| S13 | 321 | "solid state drive" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/02 10:31 |
| S14 | 87 | 714/5.ccls. and @pd> = "20061229" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/02 10:35 |
| S15 | 6 | 714/764.ccls. and @pd> = "20070105" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/02 10:36 |
| S16 | 706212 | "Integrated circuit" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/02 10:37 |
| S17 | 14018 | "Integrated circuit" and "714".das. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/02 10:57 |

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| S18 | 87 | "integrated circuit" with ECC and "714".cls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/02 10:57 |
| S19 | 115 | 714/5.ccls. and @pd> = "20070902" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/05/16 10:55 |
| S20 | 21 | 714/764.ccls. and @pd> = "20070902" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/05/19 10:12 |
| S21 | 96 | ("solid state" with (non-volatile adj memory)) and (error adj correction) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/05/19 13:52 |
| S22 | 164 | 714/773.ccls. | US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/05/19 14:21 |
| S23 | 2536 | 714/6.ccls. | US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/05/19 14:23 |
| S24 | 182 | 714/6.ccls. and "solid state" | US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/05/19 14:23 |
| S25 | 1100 | 714/30.ccls. | US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/05/19 14:26 |
| S26 | 31 | 714/30.ccls. and "solid state" | US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/05/19 14:26 |

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| S27 | 905 | 714/42.ccls. | US-PGPUB; USPAT; USOOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/05/19 14:27 |
| S28 | 68 | 714/42.ccls. and "solid state" | US-PGPUB; USPAT; USOOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/05/19 14:27 |
| S29 | 112 | 714/5.ccls. and @pd> = "20080519" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/12/03 10:31 |
| S30 | 194 | 714/6.ccls. and @pd> = "20080519" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/12/03 13:39 |
| S31 | 81 | 714/30.ccls. and @pd> = "20080519" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/12/03 14:55 |
| S32 | 77 | 714/42.ccls. and @pd> = "20080519" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/12/03 15:13 |
| S33 | 24 | 714/764.ccls. and @pd> = "20080519" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/12/03 15:16 |
| S34 | 22 | 714/773.ccls. and @pd> = "20080519" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/12/03 15:17 |
| S35 | 159298 | (memory or storage) with (correct\$4) | US-PGPUB; USPAT; USOOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/12/04 08:48 |
| S36 | 37 | S35 and "functionally independent" | US-PGPUB; USPAT; USOOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/12/04 08:48 |

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| S37 | 119 | 714/5.ccls. and @pd> = "20081203" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/22 08:37 |
| S38 | 154 | 714/6.ccls. and @pd> = "20081203" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/22 08:43 |
| S39 | 64 | 714/30.ccls. and @pd> = "20081203" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/22 08:50 |
| S40 | 68 | 714/42.ccls. and @pd> = "20081203" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/22 08:54 |
| S41 | 20 | 714/764.ccls. and @pd> = "20081203" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/22 08:56 |
| S42 | 16 | 714/773.ccls. and @pd> = "20081203" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/22 08:56 |
| S43 | 274 | (MICHELEONI-RINO RAVASIO- ROBERTO).in. | US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/22 08:58 |
| S44 | 2319 | 714/25.ccls. | US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/22 09:11 |
| S45 | 19 | 714/25.ccls. and ("solid- state memory" or "solid state memory") | US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/22 09:12 |
| S46 | 95 | ("solid-state memory" or "solid state memory") with correct\$3 | US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/22 09:19 |

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| S47 | 702 | ("solid-state memory" or "solid state memory") and (correct\$3 with circuit) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/22 09:57 |
| S48 | 41 | ("solid-state memory" or "solid state memory") and ((correct\$3 with circuit) with (first and second)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/22 09:57 |
| S49 | 2046 | (memory) with ((correct\$3 with circuit) with (first and second)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/22 12:35 |
| S50 | 324 | (memory) with ((correct\$3 with circuit) with (first and second)) and (different with error) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/22 12:36 |

5/ 22/ 09 2:55:37 PM

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